FSC-1712LD2N Series

(Full-Size Single Board Computer)

INSTALLATION GUIDE

Installation Guide Revision A.0 60002F5A0

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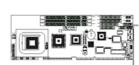




INTRODUCTION

Unpacking FSC-1712LD2N Series 1.1

- 1. Take out the FSC-1712LD2N series unit from the carton box, check if the unit is properly secure in the plastic bag.
- 2. Check the contents of the carton box:
 - ◆ Single Board Computer



◆ Installation Guide



- ◆ Floppy ribbon cable
- ◆ ATA-66/100 HDD ribbon cable

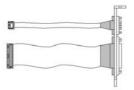




- ◆ COM2 ribbon cable



◆ Print & COM1 ribbon cable



- ◆ External K/B & M/S cable
- ◆ Driver & PC-Cillin Utility





- ◆ External K/B-M/S cable (6-pin to 6-pin)
- External ATX cable (4-pin to 4-pin)





- 3. Optional Cable
- ◆ External USB cable (optional) ◆ (optional)
- ◆ External K/B-M/S cable (6-pin to 5-pin)





1.2 Description

The FSC-1712LD2N Series single board computer is optimized for socket 478 FC-PGA processor, supporting 400MHz Front Side Bus, the memory can accommodate is up to 3GB SDRAM. This board is based on the Intel® 845 chipset and is fully designed for harsh industrial environment. The FSC-1712LD2N/LN series is with on-board ATI LCD/VGA controller and dual 10/100 Mbps Ethernet controllers. The FSC-1712LD2N/E series is with on board one 10/100 Mbps Ethernet controller. It is for CTI and high-end applications.

The other I/O function include two serial ports, one parallel port, two IDE interface, one FDC interface, four USB ports, Watch Dog Timer, DOC and PS/2 Keyboard & Mouse.

In the meantime, the key components inside are chosen on the long-term availability criterion, such as Intel[®] chipset. We guarantee this product will be available. Even longer life is still possible which is dependent on the marketing situation. We also accept to extend the product life cycle based on OEM contract.

1.3 Features

- ◆ Intel[®] Pentium[®] 4 CPU processors
- ♦ Intel[®] 845 AGPset
- ◆ Support 3GB SDRAM (Max.) Memory
- ◆ Front Side Bus Frequency: 400 MHz
- ◆ On-board ATI Mobility M6 graphic controller integrated 8MB VRAM
- ◆ Dual 10/100 Mbps Ethernet interface using Intel® 82559 & 82562EM chip
- Software programmable watchdog timer
- Hardware Monitoring

1.3.1 Hardware Monitoring

Hardware monitoring allows you to monitor various aspects of your systems operations and status. The features include CPU temperature, voltage and RPM of fan.

1.3.2 I/O Shield Connector

The SBC is equipped with an I/O bracket. Please use the appropriate I/O shield (figure 1).

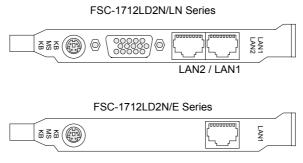


Figure 1: I/O bracket layout

1.3.3 FSC-1712LD2N Series Overview

Function / Model	FSC-1712LD2N/E	FSC-1712LD2N/LN
◆ Chipset	845	845
◆ SCSI Function	1	1
◆ LCD Function	1	•
◆ VGA Function	1	•
◆ Ethernet Function	⊚ (82562)	
◆ Two EIDE Interface	•	•
◆ One Floppy Interface	•	•
◆ Two Serial, One Parallel	•	•
◆ USB port	•	•
◆ Solid State Disk	•	•

1.3.4 System Block Diagram

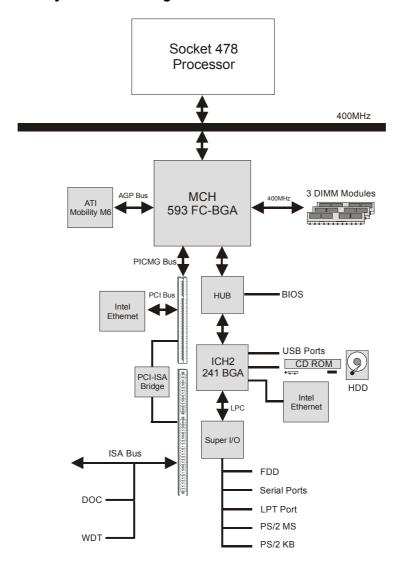


Figure 2: System Block Diagram

1.4 Specifications

FSC-1712LD2N:

♦ Processor:

- Socket 478 Processors, for Intel® Pentium® 4 Processor

♦ Chipset:

- Intel® 845 AGPset

◆ Front Side Bus:

- 400 MHz

◆ DRAM Module:

- 168pin DIMM x 3 for PC133 Memory
- Support DRAM up to 3GB
- ◆ LCD/VGA Function: (FSC-1712LD2N/LN Series)
 - ATI® Mobility M6 controller integrated 8MB VRAM

◆ Dual LAN Function: (FSC-1712LD2N/LN Series)

- Intel[®] 82559 (LAN2) & 82562EM (LAN1) controllers (FSC-1712LD2N/E only used 82562EM-LAN1)
- For 10/100Base-TX Ethernet

◆ Onboard I/O:

- On-Chip I/O integrated with K/B, Mouse, FDD, Parallel and Serial,

◆ Onboard PCI / IDE:

- Intel® 82371 Controller
- PCI rev2.2 Compliant
- ACPI Compliant Power Management
- PCI Bus IDE Port with PIO /ATA-100 x 2 (Up to 4 Devices)

♦ I/O Connector:

- PS/2 Mouse and PS/2 style Keyboard
- 15-pin D-Sub VGA connector
- Dual RJ-45 connector (FSC-1712LD2N/LN Series) (FSC-1712LD2N/E Series only single RJ-45 connector)

♦ USB Ports:

- 4 x USB ports (pin-header)

♦ BIOS:

- Award Plug & Play BIOS

- Extended Function:
 Hardware Monitoring function by W83781D
 IrDA by pin-header

♦ Form Factor:

- 13.3" x 4.8" (338 x 122mm)

♦ Weight:

- 0.93lb (422 g.w.) --- FSC-1712LD2N/LN

SECTION 2

INSTALLATIONS

2.1 System Installation

2.1.1 CPU Installation

Carefully follow the steps below in order to install the CPU:

- 1. Check and confirm that the jumpers are correctly set for the CPU you are going to install (figure 3).
- 2. Lift the releasing lever of the Socket 478.
- 3. Align the pins of the CPU against the pinholes of the Socket 478. Be sure to pay attention to the orientation of the CPU.

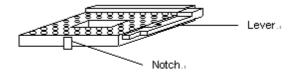


Figure 3: CPU Socket

- 4. Push down the CPU into the Socket 478.
- 5. Push down the release lever and lock it against the key hook.
- 6. Hook the hole in ZIF clip for the CPU cooling fan onto the notch on the socket 478.
- 7. Place the CPU cooling fan atop the CPU surface.
- 8. Push down the opposite side of the ZIF clip and hook it.
- 9. Slide the head of the clip to left and lock it.
- 10. Connect the cooling fan cable to the socket as shown below. Be careful not to place the cable on the CPU cooling fan.

Removing a CPU:

- 1. Before removing the CPU, turn off the FSC-1712LD2N Series power; then wait for about 20 minutes until the heat radiation plate of the cooling fan and the CPU cool down.
- 2. To remove the CPU, lift the releasing lever of the Socket 478.

Note: The CPU and the heat radiation plate are hot. They may cause

burns.

To remove the CPU, reverse the installation steps.

2.1.2 Memory Module Installation

Figure 4 display the notch marks and what they should look like on your DIMM memory module.

DIMMs have 168-pins and two notches, that will match with the onboard DIMM socket. DIMM modules are installed by placing the chip firmly into the socket at a 90-degree angle and pressing straight down (figure 5) until it fits tightly into the DIMM socket.

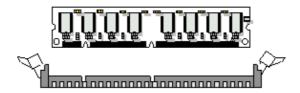


Figure 4: DIMM Memory and 168-pins Socket

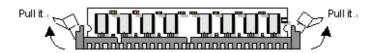


Figure 5: Memory Installation

Carefully follow the steps below in order to install the DIMMs:

- 1. To avoid generating static electricity and damaging the DIMM, ground yourself by touching a grounded metal surface or using a ground scrap before you touch the DIMM.
- 2. Do not touch the connector of the DIMM. Dirt residue may cause a malfunction.
- Hold the DIMM with its notch to the front side of the FSC-1712LD2N Series and insert it completely into the socket. A DIMM should be inserted into the inner socket first. Guiding the hole at each end of the DIMM over the retaining post at each end of the DIMM socket.
- 4. If you install two DIMMs, install the second DIMM using the same procedure as above.
- 5. If DIMM does not go in smoothly, do not force it. Pull it all the way out and try again.
- 6. Make sure the DIMM is properly installed and locked by the tabs on both sides of the socket.

Removing a DIMM:

To remove the DIMM, use your fingers or a small screwdriver to carefully push away the plastic tabs that secure the DIMM at each end. Lift it out of the socket.

Make sure you store the DIMM in an anti-static bag.

2.1.3 Setting Jumpers and DIP Switches

There are jumpers and DIP-switches on the system board of the FSC-1712LD2N Series. You can set the jumpers to make the necessary operations.

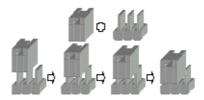


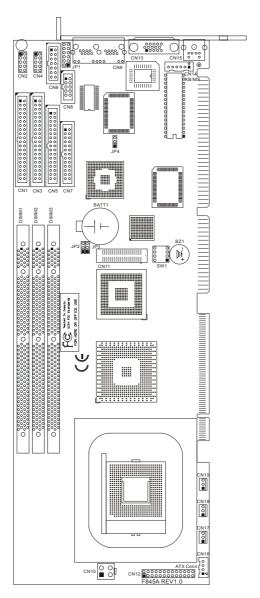
Figure 6: Jumper Connector

For any three-pin jumpers (Figure 6), the jumper setting is 1-2 when the jumper connects pins 1 and 2. The setting is 2-3 when pins 2 and 3 are connected and so on. You see a number "1" and a "3" printed on the circuit board to identify these pins. And also, there is a second way of indication — one of the lines surrounding jumpers is thick, which indicates pin NO.1.

To move a jumper from one position to another, use needle-nose pliers or tweezers to pull the pin cap off the pins and move it to the desired position.

2.2

Board LayoutJumper & Connector Location



Jumper Setting 2.3

Table for Jumper Location Description:Use the information in the following table to change the jumpers and the DIP

switches.

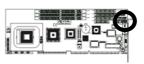
Jumpers	Functions
JP1	COM Port Setting Select
JP2	Watchdog Timer Setting Select
JP3	LVDS Voltage Select
JP4	Clear CMOS Setting Select
SW1	Panel Type Select

In order to set up the correct configuration, here is the description about how to set the jumpers to enable/disable or change functions. All jumpers' location please refer to jumper location diagram.

◆ COM Port Setting Select: JP1

Function	JP1					
Function	1-2	3-4	5-6	7-8	9-10	11-12
RS-232	Off	Off	Off	Off	Off	On
RS-422	On (Term.)	On (Term.)	Off	On	On	Off
RS-485	On (Term.)	On (Term.)	On	Off	On	Off

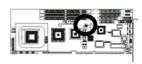
• Location:



◆ Watchdog Timer Setting Select: JP2

mady miles detaing desect of =				
Function	JP2			
NMI	1-2			
Reset System (Default)	2-3			

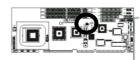
• Location:



◆ LVDS Voltage Select: JP3

Function	JP3
3.3V (Default)	1-2
5V	2-3

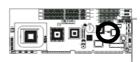
• Location:



◆ Clear CMOS Setting Select: JP4

Function	JP4
Normal (Default)	1-2
Clear CMOS	2-3

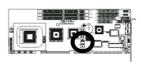
• Location:



◆ Panel Type Select: SW1

Function	SW1				
Function	1	2	3	4	
ID0 (CRT Only)	Off	Off	Off	Off	
ID1 (800 x 600)	On	Off	Off	Off	
ID2 (1024 x 768)	Off	On	Off	Off	
ID3 (1280 x 1024)	On	On	Off	Off	

• Location:



2.4 Connector's Description

Connector Location

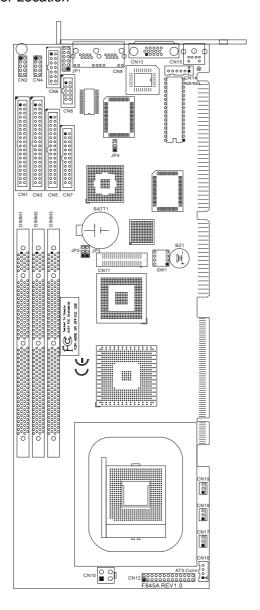
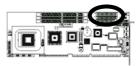


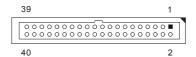
Table for Connector's Location Description:Use the information in the following table to change the connector.

Connectors	Functions
CN1	Primary IDE Connector
CN2	USB2/USB3 Port Connector
CN3	Secondary IDE Connector
CN4	USB0/USB1 Port Connector
CN5	Floppy Disk Connector
CN6	COM2 RS-232/422/485 Serial Port Connector
CN7	Parallel Port Connector
CN8	COM1 RS-232 Serial Port Connector
CN9	LAN1 (82562) / LAN2 (82559) Connector
CN10	ATX 12V Power Connector
CN11	LCD Connector
CN12	System Panel Indicate Connector
CN13	CRT VGA Port Connector
CN14	External Keyboard & Mouse Connector
CN15	PS/2 Keyboard/Mouse Connector
CN16	ATX Power Connector
CN17	System Fan Power Connector
CN18	CPU Fan1 Power Connector
CN19	CPU Fan2 Power Connector

◆ Primary IDE Connector (40-pin 2.54mm Pitch Pin-Header with Housing): CN1

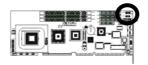
Pin#	Assignment	Pin#	Assignment
1	Reset IDE	2	Ground
3	Host Data 7	4	Host Data 8
5	Host Data 6	6	Host Data 9
7	Host Data 5	8	Host Data 10
9	Host Data 4	10	Host Data 11
11	Host Data 3	12	Host Data 12
13	Host Data 2	14	Host Data 13
15	Host Data 1	16	Host Data 14
17	Host Data 0	18	Host Data 15
19	Ground	20	NC
21	DRQ 0	22	Ground
23	Host IOW	24	Ground
25	Host IOR	26	Ground
27	IOCHRDY	28	Host ALE
29	DACK 0	30	Ground
31	IRQ 14	32	No Connect
33	Address 1	34	No Connect
35	Address 0	36	Address 2
37	Chip Select 0	38	Chip Select 1
39	Activity	40	Ground





◆ USB2/USB3 Port Connector (10-pin Pin-Header): CN2

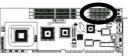
Pin#	Assignment	Pin#	Assignment					
1	VCC	2	Ground					
3	USB1 0-	4	Ground					
5	USB1 0+	6	USB2 0+					
7	Ground	8	USB2 0-					
9	Ground	10	VCC					

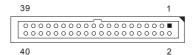




◆ Secondary IDE Connector (40-pin 2.54mm Pitch Pin-Header with Housing): CN3

Pin#	Assignment	Pin#	Assignment
1	Reset IDE	2	Ground
3	Host Data 7	4	Host Data 8
5	Host Data 6	6	Host Data 9
7	Host Data 5	8	Host Data 10
9	Host Data 4	10	Host Data 11
11	Host Data 3	12	Host Data 12
13	Host Data 2	14	Host Data 13
15	Host Data 1	16	Host Data 14
17	Host Data 0	18	Host Data 15
19	Ground	20	NC
21	DRQ 1	22	Ground
23	Host IOW	24	Ground
25	Host IOR	26	Ground
27	IOCHRDY	28	Host ALE
29	DACK 1	30	Ground
31	IRQ 15	32	No Connect
33	Address 1	34	No Connect
35	Address 0	36	Address 2
37	Chip Select 0	38	Chip Select 1
39	Activity	40	Ground

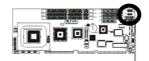




◆ USB0/USB1 Port Connector (10-pin Pin-Header): CN4

* 00D0/	• 00B0/00B11 01t 00IIIIcctor (10-piii 1 III-11caaci): 014-								
Pin#	Assignment	Pin#	Assignment						
1	VCC	2	Ground						
3	USB1 0-	4	Ground						
5	USB1 0+	6	USB2 0+						
7	Ground	8	USB2 0-						
9	Ground	10	VCC						

• Figure:

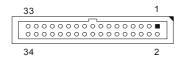




◆ Floppy Disk Connector (34-pin 2.54mm Pitch Pin-Header with Housing): CN5

	riodanig). ONa								
Pin#	Assignment	Pin#	Assignment						
1	Ground	2	Drive Density Selection						
3	Ground	4	No Connect						
5	Ground	6	Drive Density Selection						
7	Ground	8	Index						
9	Ground	10	Motor Enable 0						
11	Ground	12	Drive Select 1						
13	Ground	14	Drive Select 0						
15	Ground	16	Motor Enable 1						
17	Ground	18	Direction						
19	Ground	20	Step						
21	Ground	22	Write Data						
23	Ground	24	Write Gate						
25	Ground	26	Track 00						
27	Ground	28	Write Protect						
29	Ground	30	Read Data						
31	Ground	32	Head Select						
33	Ground	34	Diskette Change						



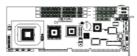


♦ COM2 RS-232/422/485 Serial Port Connector (14-pin 2.54mm Pitch

Pin-Header with Housing): CN6

Pin#	Assignment	Pin#	Assignment				
1	DCD (Data Carrier Detect)	2	RXD (Receive Data)				
3	TXD (Transmit Data)	4	DTR (Data Terminal Ready)				
5	Ground	6	DSR (Data Set Ready)				
7	RTS (Request to Send)	8	CTS (Clear to Send)				
9	RI (Ring Indicator)	10	Ground				
11	TXD+ (Transmit Data+)	12	TXD- (Transmit Data-)				
13	RXD+ (Receive Data+)	14	RXD- (Receive Data-)				

• Figure:

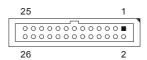




◆ Parallel Port Connector (26-pin 2.54mm Pitch Pin-Header with Housing): CN7

	riodonig/i Orti								
Pin#	Assignment	Pin#	/\ssignment						
1	Line Printer Strobe	2	PD 0, Parallel Data 0						
3	PD 1, Parallel Data 1	4	PD 2, Parallel Data 2						
5	PD 3, Parallel Data 3	6	PD 4, Parallel Data 4						
7	PD 5, Parallel Data 5	8	PD 6, Parallel Data 6						
9	PD 7, Parallel Data 7	10	ACK, Acknowledge						
11	Busy	12	Paper Empty						
13	Select	14	Auto Feed						
15	Error	16	Initialize						
17	Select	18	Ground						
19	Ground	20	Ground						
21	Ground	22	Ground						
23	Ground	24	Ground						
25	Ground	26	N/A						

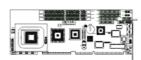




◆ COM1 RS-232 Serial Port Connector (10-pin 2.54mm Pitch Pin-Header with Housing): CN8

Pin#	Assignment	Pin#	Assignment
1	DCD (Data Carrier Detect)	2	RXD (Receive Data)
3	TXD (Transmit Data)	4	DTR (Data Terminal Ready)
5	Ground	6	DSR (Data Set Ready)
7	RTS (Request to Send)	8	CTS (Clear to Send)
9	RI (Ring Indicator)	10	NC

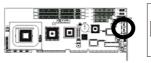
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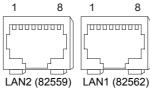




◆ LAN1 (82562) / LAN2 (82559) Connector (RJ-45 Phone-jacket): CN9

Pin#	Assignment	Pin#	Assignment
1	Transmit output (+)	5	NC
2	Transmit output (-)	6	Receive input (-)
3	Receive input (+)	7	NC
4	NC	8	NC

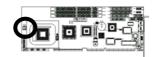




◆ ATX 12V Power Connector: CN10

Pin#	Assignment	Pin#	Assignment
1	Ground	2	Ground
3	12V	4	12V

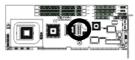
• Figure:

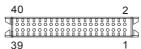




◆ LCD Connector (40-pin Male): CN11

Pin#	Assignment	Pin#	Assignment					
1	P0	2	P1					
3	P2							
5		6 P5						
7								
		8 P7						
9	Ground	10	PVCC					
11	P8	12	P9					
13	P10	14	P11					
15	P12	P13						
17	P14	18	P15					
19	Ground	20	PVCC					
21	P16	22	P17					
23	P18	24	P19					
25	P20	26	P21					
27	P22	28	P23					
29	Ground	30	PVCC					
31	FLM	32	LP					
33	SHFCLK	34	M					
35	ENAVEE	36	PVEE					
37	ENAVDD	38	P12V					
39	ENABKL	40	P12V					





◆ System Panel Indicate Connector: CN12

Pin#	Assignment	Pin # Assignment				
	IR		RESET			
1	+5V	2	RESET+			
3	FIRTX	4	RESET-			
5	IRRX		SPI:AKER			
7	Ground	8	SPKR (Default)			
9	IRTX	10 BUZZ (Default)				
	HDD LED	12 Ground				
13	HDLED+	14	+5V			
15	HDLED-		PWR LED			
	PW/R ON	18	PWLED+			
23	PWRBT+	20	NC			
25	PWRBT-	22 PWLED-				
			KEYLOCK			
		24	KBLOCK			
		26	Ground			

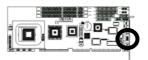
• Figure:

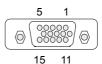


2												26
<u>○</u>	0	0	0	0	0	0	0	0	0	0	0	00
1												25

◆ CRT VGA Port Connector (D-SUB 15-pin Female): CN13

VOICE VOAT OIL Connector (D-COD 13-pin Female). OIL 13			
Pin#	Assignment	Pin#	Assignment
1	Red Color Signal	2	Green Color Signal
3	Blue Color Signal	4	NC
5	Ground	6	Ground
7	Ground	8	Ground
9	NC	10	Ground
11	NC	12	VGA DDA
13	H-Sync.	14	V-Sync.
15	SPCLK		

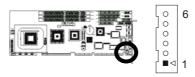




◆ External Keyboard & Mouse Connector (6-pin): CN14

Pin#	Assignment	Pin#	Assignment
1	Mouse Clock	2	Mouse Data
3	Keyboard Clock	4	Keyboard Data
5	Ground	6	+5V

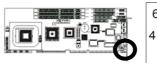
• Figure:



◆ PS/2 Keyboard/Mouse Connector (Mini Din 6 Pin): CN15

Pin#	Assignment	Pin#	Assignment
1	Keyboard Data	2	Mouse Data
3	Ground	4	+5V
5	Keyboard Clock	6	Mouse Clock

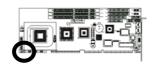
• Figure:





◆ ATX Power Connector: CN16

Pin #	/ ssignment
1	PME
2	5VSB
3	PSON
4	Ground

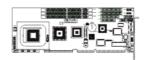




◆ System Fan Power Connector: CN17

Pin #	/ ssignment
1	Ground
2	+12V
3	Fan Status Signal

• Figure:

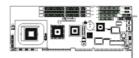




◆ CPU Fan1 Power Connector: CN18

Pin #	/ ssignment
1	Ground
2	+12V
3	Fan Status Signal

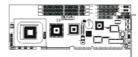
• Figure:





◆ CPU Fan2 Power Connector: CN19

Pin #	/ ssignment
1	Ground
2	+12V
3	Fan Status Signal





SECTION 3

AWARD BIOS SETUP

3.1 BIOS Instructions

Award's ROM BIOS provides a built-in Setup program, which allows user to modify the basic system configuration and hardware parameters. The modified data will be stored in a battery-backed CMOS, so that data will be retained even when the power is turned off. In general, the information saved in the CMOS RAM will stay unchanged unless there is a configuration change in the system, such as hard drive replacement or a device is added.

It is possible for the CMOS battery to fail, this will cause data loss in the CMOS only. If this does happen you will need to reconfigure your BIOS settings.

3.2 Main Menu

Once you enter the AwardBIOS™ CMOS Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions and two exit choices. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

CMOS Setup Utility - Copyright © 1984 - 2001

Standard CMOS Feature Advanced BIOS Feature Advanced Chipset Feature Integrated Peripherals Power Management Setup PnP/PCI Configurations PC Health Status	Frequency/Voltage Control Load Fail-Safe Defaults Load Optimized Defaults Set Supervisor Password Set User Password Save & Exit Setup Exit Without Saving	
Esc: Quit F6: Save CMOS To BIOS F10: Save & Exit Setup	↑↓←→: Select Item F7: Load CMOS From BIOS	
Time, Date, Hard Disk Type		

Note: that a brief description of each highlighted selection appears at the bottom of the screen.

Setup Items:

The main menu includes the following main setup categories. Recall that some systems may not include all entries.

Standard CMOS Features:

Use this menu for basic system configuration. See Section 2 for the details.

Advanced BIOS Features:

Use this menu to set the Advanced Features available on your system. See Section 3 for the details.

Advanced Chipset Features:

Use this menu to change the values in the chipset registers and optimize your system's performance. See section 4 for the details.

Integrated Peripherals:

Use this menu to specify your settings for integrated peripherals. See section 4 for the details.

Power Management Setup:

Use this menu to specify your settings for power management. See section 5 for the details.

PnP / PCI Configuration:

This entry appears if your system supports PnP / PCI. See section 6 for the details.

PC Health Status:

Use this menu to show your system temperature, speed and voltage status.

Frequency/Voltage Control:

Use this menu to specify your settings for frequency/voltage control. See section 7 for the details.

Load Fail-Safe Defaults:

Use this menu to load the BIOS default values for the minimal/stable performance for your system to operate. See section 8 for the details.

Load Optimized Defaults:

Use this menu to load the BIOS default values that are factory settings for optimal performance system operations. While Award has designed the custom BIOS to maximize performance, the factory has the right to change these defaults to meet their needs. See section 8 for the details.

Supervisor/User Password:

Use this menu to set User and Supervisor Passwords. See section 9 for the details.

Save & Exit Setup:

Save CMOS value changes to CMOS and exit setup. See section 10 for the details.

Exit Without Save:

Abandon all CMOS value changes and exit setup. See section 10 for the details.

3.3 Standard CMOS Features

The items in Standard CMOS Setup Menu are divided into 10 categories. Each category includes no, one or more than one setup items. Use the arrow keys to highlight the item and then use the <PgUp> or <PgDn> keys to select the value you want in each item.

Date (mm:dd:yy): Time (hh:mm:ss):	Mon, Aug 6 2001 16:19:20	Item Help	
➤ IDE Primary Master ➤ IDE Primary Slave ➤ IDE Secondary Master ➤ IDE Secondary Slave Drive A Drive B Video Halt On Based Memory Extended Memory Total Memory	2557 MB None None None 1.44M, 3.5 in. None EGA/VGA All Errors 640K 64512K 65536K	Menu Level ➤ Change the day, month, year and century	
↑↓←→Move Enter: Select +/-/PU/PD: Value F10: Save ESC: Exit F1: General Help F5: Previous Values F6: Fail-safe defaults F7: Optimized Defaults			

Date: Options Month/DD/YYYY

Set the system date. Note that the 'Day' automatically changes when you set the date.

Time: Options HH: MM: SS

Set the system time.

IDE Primary Master: Options are in its sub menu (described in Table 3) Press <Enter> to enter the sub menu of detailed options.

IDE Primary Slave: Options are in its sub menu (described in Table 3) Press <Enter> to enter the sub menu of detailed options.

IDE Secondary Master: Options are in its sub menu (described in Table 3) Press <Enter> to enter the sub menu of detailed options.

IDE Secondary Master: Options are in its sub menu (described in Table 3) Press <Enter> to enter the sub menu of detailed options.

Drive A/ Drive B: Options None 360K, 5.25 in/1.2M, 5.25 in/720K, 3.5 in/1.44M, 3.5 in/2.88M, 3.5 in Select the type of floppy disk drive installed in your system.

Video: Options EGA/VGA/CGA 40/CGA 80/MONO Select the default video device.

Halt On: Options All Errors/No Errors/All, but Keyboard/All, but Diskette/All,but Disk/Key Select the situation in which you want the BIOS to stop the POST process and notify you.

Base Memory: Option N/A

Displays the amount of conventional memory detected during boot up.

Extended Memory: Option N/A

Displays the amount of extended memory detected during boot up

Total Memory: Option N/A

Displays the total memory available in the system

3.4 IDE Adapters

The IDE adapters control the hard disk drive. Use a separate sub menu to configure each hard disk drive.

CMOS Setup Utility – Copyright © 1984 - 2001 Award Software IDE Primary Master

	IDETT	,		
IDE HDD Auto-Detection	Press Enter		Item He	lp
IDE Primary Master	Auto 2557 MB Auto	Menu Level	>>	
Access Mode	rato	To auto-detect th	ne HDD's s	ize.
	4956	head on this ch	nannel	-,
Cylinder	16			
Head	0			
Precomp	4955			
Landing Zone	63			
Sector				
↑↓←→Move Enter: Select +/-/PU/PD: Value F10: Save ESC: Exit F1: General Help				
F5: Previous Values	F6: Fail-safe default	s F7: Optimized	Defaults	

IDE HDD Auto-detection: Options Press Enter

Press Enter to auto-detect the HDD on this channel. If detection is successful, it fills the remaining fields on this menu.

IDE Primary Master: Options None, Auto and Manual

Selecting "Manual" lets you set the remaining fields on this screen. Selects the type of fixed disk. "User Type" will let you select the number of cylinders, heads, etc. Note: PRECOMP=65535 means NONE!

Capacity: Options Auto Display your disk drive size

Disk drive capacity (Approximated). Note that this size is usually slightly greater than the size of a formatted disk given by a disk-checking program.

Access Mode: Options Normal, LBA, Large and Auto

Choose the access mode for this hard disk

Award BIOS Setup

The following options are selectable only if the 'IDE Primary Master' item is set to 'Manual'

Cylinder: Options Min = 0, Max = 65535 Set the number of cylinders for this hard disk.

Head: Options Min = 0, Max = 255 Set the number of read/write heads

Precomp: Options Min = 0, Max = 65535

**** Warning: Setting a value of 65535 means no hard disk

Landing zone: Options Min = 0, Max = 65535

Sector: Options Min = 0, Max = 255

Number of sectors per track

3.5 Advanced BIOS Features

This section allows you to configure your system for basic operation. You have the opportunity to select the system's default speed, boot-up sequence, keyboard operation, shadowing and security.

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Advanced BIOS Features

	Auvanceu D	10010	atures	<u> </u>			
Virus Warning CPU L1 & L2 Cache	Disabled Enabled			Item	Help		
Quick Power On Self Test	Enabled						_
First Boot Device	Floppy		Menu	ı Level	>		
Second Boot Device	HDD-0						
Third Boot Device	LS-120		Allow	you to cl	noose	the VI	RUS
Boot Other Device	Enabled		warn	ing feature for	IDE Ha	ard Disk	boot
Swap Floppy Drive	Disabled		secto	r protection.	If this	s function	on is
Boot Up Floppy Seek	Enabled		enab	led and some	one atte	empt to	write
Boot Up NumLock Status	On		data	into this area	i, BIOS	will sh	ow a
Gate A20 Option	Fast		warn	ing message	e on	screen	and
Typematic Rate Setting	Disabled		alarm	n beep			
X Typematic Rate (Chars/Sec)	6						
X Typematic Delay (Msec)	250						
Security Option	Setup						
APIC Mode	Disabled						
X MPS Version Control For OS	1.4						
OS Select For DRAM > 64MB	Non-OS2						
Report No FDD For Win 95	No						
C8000 - CBFFF Shadow	Disabled						
CC000 - CFFFF Shadow	Disabled						
D0000 - D3FFF Shadow	Disabled						
D4000 - D7FFF Shadow	Disabled						
D8000 - DBFFF Shadow	Disabled						
DC000 - DFFFF Shadow	Disabled						
Small Logo (EPA) Show	Enabled						
↑↓←→Move Enter: Select +/-/Pi	U/PD: Value	F10: S	Save	ESC: Exit	F1: Ge	neral H	elp
F5: Previous Values F6: Fail-s	afe defaults	F7: On	otimize	ed Defaults			

Virus Warning:

Allow you to choose the VIRUS Warning feature for IDE Hard Disk boot sector protection. If this function is enabled and someone attempts to write data into this area, BIOS will show a warning message on screen and alarm beep.

Enabled---Activates automatically when the system boots up causing a warning message to appear when anything attempts to access the boot sector or hard disk partition table.

Disabled---No warning message will appear when anything attempts to access the boot sector or hard disk partition table.

CPU L1& L2 Cache:

These two categories speed up memory access. However, it depends on CPU/chipset design.

Enabled---Enable cache Disabled---Disable cache

Quick Power On Self Test:

This category speeds up Power On Self Test (POST) after you power up the computer. If it is set to Enable, BIOS will shorten or skip some check items during POST.

Enabled---Enable quick POST Disabled--- Normal POST

First/Second/Third/Other Boot Device:

The BIOS attempts to load the operating system from the devices in the sequence selected in these items.

The Choice: Floppy, LS/ZIP, HDD, SCSI, CDROM and Disabled.

Swap Floppy Drive:

If the system has two floppy drives, you can swap the logical drive name assignments.

The choice: Enabled, Disabled.

Boot Up Floppy Seek:

Seeks disk drives during boot up. Disabling speeds boot up.

The choice: Enabled, Disabled.

Boot Up NumLock Status:

Select power on state for NumLock. The choice: Enabled, Disabled.

Gate A20 Option:

Select if chipset or keyboard controller should control GateA20. Normal---A pin in the keyboard controller controls GateA20 Fast---Lets chipset control GateA20

Typematic Rate Setting:

Keystrokes repeat at a rate determined by the keyboard controller. When enabled, the typematic rate and typematic delay can be selected.

The choice: Enabled, Disabled.

Typematic Rate (Chars/Sec):

Sets the number of times a second to repeat a keystroke when you hold the key down.

The choice: 6, 8, 10, 12, 15, 20, 24 and 30.

Typematic Delay (Msec):

Sets the delay time after the key is held down before it begins to repeat the keystroke.

The choice: 250, 500, 750 and 1000.

Security Option:

Select whether the password is required every time the system boots or only when you enter setup.

System---The system will not boot and access to Setup will be denied if the correct password is not entered at the prompt. Setup---The system will boot, but access to Setup will be denied if the correct password is not entered at the prompt.

Note: To disable security, select PASSWORD SETTING at Main Menu and then you will be asked to enter password. Do not type anything and just press <Enter>, it will disable security. Once the security is disabled, the system will boot and you can enter Setup freely.

APIC Mode:

This item allows you to enable/disable APIC Mode.

MPS Version Control For OS:

Select the operating system that is Multi-Processors Version Control for OS.

The choice: 1.4, 1.1.

OS Select For DRAM > 64MB:

Select the operating system that is running with greater than 64MB of RAM on the system.

The choice: Non-OS2, OS2.

Report No FDD For Win 95:

Whether report no FDD for Win 95 or not.

The choice: Yes, No.

C8000 - CBFFF/ DC000 - DFFFF Shadow:

Shadowing a ROM reduces the memory available between 640KB to 1024KB. These fields determine whether optional ROM will be copied to RAM or not.

The choice: Enabled, Disabled.

Small Logo (EPA) Show:

Set the Logo (EPA) show or not.
The choice: Enabled, Disabled.

3.6 Advanced Chipset Features

This section allows you to configure the system based on the specific features of the installed chipset. This chipset manages bus speeds and access to system memory resources, such as DRAM and the external cache. It also coordinates communications between the conventional ISA bus and the PCI bus. It must be stated that these items should never need to be altered. The default settings have been chosen because they provide the best operating conditions for your system. The only time you might consider making any changes would be if you discovered that data was being lost while using your system.

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Advanced Chinset Features

	Advanced Chipset F	catules	
DRAM Timing Selectable X CAS Latency Time	By SPD	Ite	m Help
X Active to Precharge Delay X DRAM RAS# to CAS# Delay X DRAM RAS# Precharge DRAM Data Integrity	6 3 3 Non-ECC	Menu Level	>
Memory Frequency For DRAM Read Thermal Mgmt System BIOS Cacheable Video BIOS Cacheable Video RAM Cacheable Memory Hole At 15M-16M Delayed Transaction Delay Prior to Thermal AGP Aperture Size (MB) Disk On Chip Address	Auto Disabled Enabled Disabled Disabled Disabled Enabled Enabled 16Min 64 DC000H-DFFFFH		
↑↓←→Move Enter: Select +/-/PU/PD: Value F10: Save ESC: Exit F1: General Help			
F5: Previous Values F6: Fail-sa	ate detaults = F7: Op	otimized Defaults	

DRAM Timing Selectable:

Select the operating system that is selecting DRAM timing, so select SPD for setting SDRAM timing by SPD.

The choice: Manual, SPD

CAS Latency Time:

When synchronous DRAM is installed, the number of clock cycles of CAS latency depends on the DRAM timing.

The choice: 2, 3

Active To Precharge Delay:

Select the operating system that is active to precharge delay.

The choice: 6, Auto.

DRAM RAS# to CAS# Delay:

You can select RAS to CAS Delay time in HCLKs of 2/2 or 3/3.

The system board designer should set the values in this field, depending on the DRAM installed. Do not change the values in this field unless you change specifications of the installed DRAM or the installed CPU.

The choice: 2, 3 and Auto.

DRAM RAS# Precharge:

If an insufficient number of cycles are allowed for the RAS to accumulate its charge before DRAM refresh, the refresh may be incomplete and the DRAM may fail to retain data. Fast gives faster performance; and Slow gives more stable performance. This field applies only when synchronous DRAM is installed in the system.

The choice: 2, 3 and Auto.

DRAM Data Integrity:

Select Parity or ECC (error-correcting code), according to the type of installed DRAM

The choice: Non-ECC, ECC.

Memory Frequency For:

You can use this item to select operating frequency for the main system memory.

The choice: Auto, 100MHz and 133MHz

DRAM Read Thermal Mgmt:

You can use this item to read the DRAM thermal.

The choice: Enabled, Disabled.

System BIOS Cacheable:

Selecting "Enabled" allows caching of the system BIOS ROM at F0000h-FFFFFh, resulting in better system performance. However, if any program writes to this memory area, a system error may result.

Video BIOS Cacheable:

Select "Enabled" allows caching of the video BIOS, resulting in better system performance. However, if any program writes to this memory area, a system error may result.

The choice: Enabled, Disabled.

Video RAM Cacheable:

Select "Enabled" allows caching of the video RAM, resulting in better system performance.

The choice: Enabled, Disabled.

Memory Hole At 15M-16M:

You can reserve this area of system memory for ISA adapter ROM. When this area is reserved, it cannot be cached. The user information of peripherals that need to use this area of system memory usually discusses their memory requirements.

The choice: Enabled, Disabled.

Delayed Transaction:

The chipset has an embedded 32-bit posted write buffer to support delay transactions cycles. Select "Enabled" to support compliance with PCI specification version 2.1.

The choice: Enabled, Disabled.

Delay Prior to Thermal:

Select this item allows the delay prior to thermal time.

The choice: Auto, 64Min

AGP Aperture Size (MB):

This field determines the effective size of the Graphic Aperture used for a particular GMCH configuration. It can be updated by the GMCH-specific BIOS configuration sequence before the PCI standard bus enumeration sequence takes place. If it is not updated then a default value will select an aperture of maximum size.

The choice: 4, 8, 16, 32, 64, 128 and 256

Disk On Chip Address:

Select this item allows the Disk On Chip address at D0000H-DFFFFH.

The choice: D0000H-DFFFFH, D4000H-D7FFFH, D8000H-DBFFFH, DC000H-DFFFFH

3.7 Integrated Peripherals

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Integrated Peripherals

	integrated i emp		
OnChip Primary PCI IDE IDE Primary Master PIO IDE Primary Slave PIO IDE Primary Master UDMA IDE Primary Slave UDMA OnChip Secondary PCI IDE IDE Secondary Master PIO IDE Secondary Slave PIO	Enabled Auto Auto Auto Auto Enabled Auto Auto Auto	Item Help Menu Level > If your IDE hard drive supports block mode select Enabled for automatic detection of the optimal number of block read/write per sector the drive	
IDE Secondary Master UDMA IDE Secondary Slave UDMA USB Controller USB Keyboard Support USB Mouse Support Init Display First IDE HDD Block Mode Power ON Function	Auto Auto Enabled Disabled Disabled AGP Enabled Button Only	can support	
X KB Power ON Password X Hot Key Power ON Onboard FDC Controller Onboard Serial Port 1 Onboard Serial Port 2 X RxD, TxD Active X IR Transmission Delay X UR2 Duplex Mode	Enter Ctrl-F1 Enabled 3F8/IRQ4 2F8/IRQ3 Hi, Lo Enabled Half		
·	IR-Rx2Tx2 378/IRQ7 SPP EPP1.7 3 Off		
↑↓←→Move Enter: Select +/-/PU/PD: Value F10: Save ESC: Exit F1: General Help F5: Previous Values F6: Fail-safe defaults F7: Optimized Defaults			

OnChip Primary/Secondary PCI IDE:

The integrated peripheral controller contains an IDE interface with support for two IDE channels. Select "Enabled" to activate each channel separately.

IDE Primary/Secondary Master/Slave PIO:

The four IDE PIO (Programmed Input/Output) fields let you set a PIO mode (0-4) for each of the four IDE devices that the onboard IDE interface supports. Modes 0 through 4 provide successively increased performance. In Auto mode, the system automatically determines the best mode for each device.

The choice: Auto, Mode 0, Mode 1, Mode 2, Mode 3 and Mode 4.

IDE Primary/Secondary Master/Slave UDMA:

Ultra DMA/33 implementation is possible only if your IDE hard drive supports it and the operating environment includes a DMA driver (Windows 95 OSR2 or a third-party IDE bus master driver). If your hard drive and your system software both support Ultra DMA/33, select "Auto" to enable BIOS support.

The choice: Auto, Disabled.

USB Controller:

Select "Enabled" if your system contains a Universal Serial Bus (USB) controller and you have USB peripherals.

The choice: Enabled, Disabled,

USB Keyboard Support:

Select "Enabled" if your system contains a Universal Serial Bus (USB) controller and you have a USB keyboard.

The choice: Enabled, Disabled.

USB Mouse Support:

Select "Enabled" if your system contains a Universal Serial Bus (USB) controller and you have a USB mouse.

The choice: Enabled, Disabled.

Init Display First:

This item allows you to decide to active whether PCI Slot or on-chip VGA first.

The choice: PCI Slot, Onboard.

IDE HDD Block Mode:

Block mode is also called block transfer, multiple commands, or multiple sector read/write. If your IDE hard drive supports block mode (most new drives do), select Enabled for automatic detection of the optimal number of block read/writes per sector the drive can support.

The choice: Enabled, Disabled

Power ON Function:

You can use this item to select operating Power On the system.

The choice: Button Only

KB Power ON Password:

You can use this item to limit operating KB Power On system.

The choice: Enter

Hot Key Power ON:

You can use this item to select operating Hot Key to Power On system.

The choice: Ctrl-F1

Onboard FDC Controller:

Select "Enabled" if your system has a floppy disk controller (FDC) installed on the system board and you wish to use it. If you install and-in FDC or the system has no floppy drive, select Disabled in this field.

The choice: Enabled, Disabled.

Onboard Serial Port 1/Port 2:

Select an address and corresponding interrupt for the first and second serial ports.

The choice: 3F8/IRQ4, 2E8/IRQ3, 3E8/IRQ4, 2F8/IRQ3, Disabled and Auto.

RxD, TxD Active:

This item allows you to determine the active of RxD, TxD.

The Choice: "Hi, Hi", "Lo, Lo", "Lo, Hi" and "Hi, Lo".

IR Transmission delay:

This item allows you to enable/disable IR transmission delay.

UR2 Duplex Mode:

This item allows you to select the IR half/full duplex function.

The choice: Half, Full.

Use IR Pins:

This item allows you to select IR transmission routes, one is RxD2m, TxD2 (COM Port) and the other is IR-Rx2Tx2.

The choice: IR-Rx2Tx2, RxD2 and TxD2.

Onboard Parallel Port:

This item allows you to determine access onboard parallel port controller with which I/O address.

The choice: 3BC/IRQ7, 378/IRQ7, 278/IRQ5 and Disabled.

Parallel Port Mode:

Select an operating mode for the onboard parallel (printer) port. Select Normal, Compatible, or SPP unless you are certain your hardware and software both support one of the other available modes.

The choice: SPP, EPP, ECP and ECP+EPP.

EPP Mode Select:

Select EPP port type 1.7 or 1.9. The choice: EPP1.7, 1.9.

ECP Mode Use DMA:

Select a DMA channel for the parallel port for use during ECP mode.

The choice: 3, 1.

PWRON After PWR-Fail:

This item allows you to select if you want to power on the system after power failure.

The choice: Off, On and Former-Sts.

3.8 Power Management Setup

The Power Management Setup allows you to configure you system to most effectively save energy while operating in a manner consistent with your own style of computer use.

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Power Management Setup

<u> </u>	-ower Managemen	it Octup	
ACPI Function ACPI Suspend	Enabled S1 (POS)	Ite	em Help
X Run VGABIOS if S3 Resume	Auto		
Power Management	User Define	Menu Level	>
Video Off Method	DPMS		
Video Off In Suspend	Yes		
Suspend Type	Stop Grant		
MODEM Use IRQ	3 Disabled		
Suspend Mode HDD Power Down	Disabled		
Soft-Off by PWR-BTTN	Instant-Off		
CPU THRM-Throttling	50.0%		
Wake-Up by PCI card	Enabled		
Power On By Ring	Enabled		
USB K/B WakeUp Form S3 (S4)	Disabled		
Resume by Alarm	Disabled		
X Date (of Month) Alarm	0		
X Time (hh:mm:ss) Alarm	0 0 0		
Reload Global Timer Events			
Primary IDE 0	Disabled		
Primary IDE 1	Disabled		
Secondary IDE 0	Disabled		
Secondary IDE 1	Disabled		
FDD, COM, LPT Port	Disabled		
PCI PIRQ [A-D]#	Disabled		
↑↓←→Move Enter: Select +/-/PU/PD: Value F10: Save ESC: Exit F1: General Help			
F5: Previous Values F6: Fail-safe defaults F7: Optimized Defaults			

ACPI Function:

This item allows you to enable/disable the Advanced Configuration and Power Management (ACPI).

ACPI Suspend:

This item allows you to select S1 (POS) or S3 (STR) function.

The choice: S1 (POS), S3 (STR).

Run VGABIOS if S3 Resume:

This item allows you to select run VGABIOS on S3 mode.

The choice: Auto

Power Management:

This category allows you to select the type (or degree) of power saving and is directly related to the following modes:

- 1. HDD Power Down
- 2. Doze Mode
- 3. Suspend Mode

Min. Power Saving:

Minimum power management. Doze Mode = 1 hr. Standby Mode = 1 hr., Suspend Mode = 1 hr., and HDD Power Down = 15 min.

Max. Power Saving:

Maximum power management -- **ONLY AVAILABLE FOR SL CPU's**. Doze Mode = 1 min., Standby Mode = 1 min., Suspend Mode = 1 min., and HDD Power Down = 1 min.

User Defined:

Allow you to set each mode individually. When not disabled, each of the ranges is from 1 min. to 1 hr. except for HDD Power Down, which ranges from 1 min. to 15 min. and disable.

Video Off Method:

This determines the manner in which the monitor is blanked.

V/H SYNC+Blank:

This selection will cause the system to turn off the vertical and horizontal synchronization ports and write blanks to the video buffer.

Blank Screen:

This option only writes blanks to the video buffer.

DPMS:

Initial display power management signaling.

Video Off Method:

This item allows you to on/off Method function.

The choice: Yes, No.

Video Off In Suspend:

This determines the manner in which the monitor is blanked.

The choice: Yes, No.

Suspend Type:

Select the Suspend Type.

The choice: PWRON Suspend, Stop Grant.

MODEM Use IRQ:

This determines the IRQ in which the MODEM can use.

The choice: 3, 4, 5, 7, 9, 10, 11 and NA.

Suspend Mode:

When "Enabled" and after the set time of system inactivity. All devices except the CPU will be shut off.

The choice: Enabled, Disabled.

HDD Power Down:

When "Enabled" and after the set time of system inactivity, the hard disk drive will be powered down while all other devices remain active.

The choice: Enabled, Disabled.

Soft-Off by PWR-BTTN:

Pressing the power button for more than 4 seconds forces the system to enter the Soft-Off state when the system has "hung."

The choice: Delay 4 Sec, Instant-Off.

CPU THRM-Throttling:

Select the CPU THRM-Throttling rate.

The choice: 12.5%, 25.0%, 37.5%, 50.0%, 62.5%, 75.0% and 87.5%.

Wake-Up by PCI Card:

An input signal from PME on the PCI card awakens the system from a soft off state.

The choice: Enabled, Disabled.

Power On by Ring:

An input signal on the serial Ring Indicator (RI) line (in other words, an incoming call on the modem) awakens the system from a soft off state.

The choice: Enabled, Disabled.

USB K/B WalkUp Form S3 (S4):

An input signal from PME on the USB awakens the system from a soft off state.

The choice: Enabled, Disabled.

Resume by Alarm:

When "Enabled", your can set the date and time at which the RTC (real-time clock) alarm awakens the system from Suspend mode.

The choice: Enabled, Disabled.

PM Events:

PM events are I/O events whose occurrence can prevent the system from entering a power saving mode or can awaken the system from such a mode. In effect, the system remains alert for anything, which occurs to a device, which is configured as Enabled, even when the system is in a power down mode.

Primary IDE 0
Primary IDE 1
Secondary IDE 0
Secondary IDE 1
FDD, COM, LPT Port
PCI PIRQ [A-D] #

3.9 PnP/PCI Configurations

This section describes configuring the PCI bus system. PCI, or **P**ersonal **C**omputer **I**nterconnect, is a system, which allows I/O devices to operate at speeds nearing the speed the CPU itself uses when communicating with its own special components. This section covers some very technical items and it is strongly recommended that only experienced users should make any changes to the default settings.

CMOS Setup Utility – Copyright © 1984 - 2001 Award Software PnP/PCI Configurations

PNP OS Installed Reset Configuration Data	No Disabled	Item Help	
Resources Controlled By X IRQ Resources	Auto (ESCD) Press Enter	Menu Level ≻	
PCI/VGA Palette Snoop	Disabled	Default is Disabled. Select Enabled to reset Extended System Configuration Data (ESCD) when you exit Setup if you have installed a new add-on and the system reconfiguration has caused such a serious conflict that the OS cannot boot	
↑↓←→Move Enter: Select +/-/PU/PD: Value F10: Save ESC: Exit F1: General Help			
F5: Previous Values F6: Fail-safe defaults F7: Optimized Defaults			

PNP OS Installed:

This item allows you to determine install PnP OS or not.

The choice: Yes, No.

Reset Configuration Data:

Normally, you leave this field Disabled. Select "Enabled" to reset Extended System Configuration Data (ESCD) when you exit Setup if you have installed a new add-on and the system reconfiguration has caused such a serious conflict that the operating system cannot boot.

Resource controlled by:

The Award Plug and Play BIOS has the capacity to automatically configure all of the boot and Plug and Play compatible devices. However, this capability means absolutely nothing unless you are using a Plug and Play operating system such as Windows[®]95. If you set this field to "Manual" choose specific resources by going into each of the sub menu that follows this field (a sub menu is preceded by a " \triangleright ").

The choice: Auto (ESCD), Manual.

IRQ Resources:

When resources are controlled manually, assign each system interrupt a type, depending on the type of device using the interrupt.

PCI/VGA Palette Snoop:

Leave this field at "Disabled".

3.10 PC Health Status

CMOS Setup Utility – Copyright © 1984 - 2001 Award Software PC Health Status

CPU Warning Temperature Fan Power After WakeUp	Disabled High	It	em Help
Fan Power In Suspend	Off		
Current System Temp.	53°C / 127°F	Menu Level	>
Current CPU Temp.	38°C / 100°F		
Current CPU Fan Speed	3835 RPM		
Current CHASSIS Fan Speed	4725RPM		
Current Power Fan Speed	3918RPM		
Vtt (V)	1.80V		
Vcore (V)	1.71V		
3.3V	3.37V		
+ 5 V	5.08V		
+12 V	11.91V		
-12 V	-12.44		
-5V	-4.99V		
VBAT (V)	3.24V		
5VSB (V)	4.80		
Shutdown temperature	Disabled		
,			
↑↓←→Move Enter: Select +/-/PU/PD: Value F10: Save ESC: Exit F1: General Help			
F5: Previous Values F6: Fail-safe defaults F7: Optimized Defaults			

CPU Warning Temperature:

This item will prevent CPU from overheating.

The choice: Enabled, Disabled.

Current System Temp:

Show you the current system temperature.

Current CPU Temp:

Show you the current CPU temperature.

Current CPU Fan Speed:

Show you the current CPU fan operating speed.

Current CHASSIS Fan Speed:

Show you the current chassis fan operating speed.

Current Power Fan Speed:

Show you the current Power fan operating speed.

IN0/1/2 (V):

Show you the voltage of Vin (0)/(1)/(2).

3.3V/+5V/+12V/-12V/-5V:

Show you the voltage of 3.3V/+5V/+12V/-12V/-5V.

Shutdown Temperature:

This item allows you to set up the CPU shutdown Temperature. This item is only effective under Windows® 98 ACPI mode.

The choice: Disabled, 60°C / 140°F , 65°C / 149°F , 70°C / 159°F and 75°C / 167°F .

3.11 Frequency/Voltage Control

CMOS Setup Utility - Copyright © 1984 - 2001 Award Software

Frequency/Voltage Control

CPU Ratio X CPU Clock Ratio	CPU Default	Item Help
Auto Detect PCI Clk Spread Spectrum	Enabled Disabled	Menu Level ≻
↑↓←→Move Enter: S	elect +/-/PU/PD: Value	F10: Save ESC: Exit F1: General Help
F5: Previous Values	F6: Fail-safe defaults	F7: Optimized Defaults

CPU Ratio:

This item allows you to select the CPU ratio, if the CPU ratio is fixed. This item was no function.

The choice: 8.X ~ 24.X and Default.

CPU Clock Ratio:

This item allows you to set up the CPU clock ratio, but this function depends on different CPU performance. It is only effective for those clock ratio haven't been locked.

The choice: X3.0 ~ X8.0.

Auto Detect PCI CLK:

When "Enabled", this item will auto detect if the PCI socket have devices and will send clock signal to PCI devices. When disabled, it will send the clock signal to all PCI socket.

The choice: Enabled, Disabled.

Spread Spectrum:

This item allows you to enable/disable the spread spectrum modulated.

3.12 Load Fail-Safe Defaults

When you press <Enter> on this item you get a confirmation dialog box with a message similar to:

Load Fail-Safe Defaults (Y/N)? N

Pressing "Y" loads the BIOS default values for the most stable, minimal-performance system operations.

3.13 Load Optimized Defaults

When you press <Enter> on this item you get a confirmation dialog box with a message similar to:

Load Optimized Defaults (Y/N)? N

Pressing "Y" loads the default values that are factory settings for optimal performance system operations.

3.14 Supervisor/User Password Setting

You can set either supervisor or user password, or both of then. The differences between are:

Set Supervisor Password: can enter and change the options of the setup menus.

Set User Password: just can only enter but do not have the right to change the options of the setup menus. When you select this function, the following message will appear at the center of the screen to assist you in creating a password.

ENTER PASSWORD:

Type the password, up to eight characters in length, and press <Enter>. The password typed now will clear any previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter a password.

To disable a password, just press <Enter> when you are prompted to enter the password. A message will confirm the password will be disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

PASSWORD DISABLED.

When a password has been enabled, you will be prompted to enter it every time you try to enter Setup. This prevents an unauthorized person from changing any part of your system configuration.

Additionally, when a password is enabled, you can also require the BIOS to request a password every time your system is rebooted. This would prevent unauthorized use of your computer.

You determine when the password is required within the BIOS Features Setup Menu and its Security option (see Section 3). If the Security option is set to "System", the password will be required both at boot and at entry to Setup. If set to "Setup", prompting only occurs when trying to enter Setup.

3.15 Exit Selecting

Save & Exit Setup

Pressing <Enter> on this item asks for confirmation:

Save to CMOS and EXIT (Y/N)? Y

Pressing "Y" stores the selections made in the menus in CMOS - a special section of memory that stays on after you turn your system off. The next time you boot your computer, the BIOS configures your system according to the Setup selections stored in CMOS. After saving the values the system is restarted again.

Exit Without Saving

Pressing <Enter> on this item asks for confirmation:

Quit without saving (Y/N)? Y

This allows you to exit Setup without storing in CMOS any change. The previous selections remain in effect. This exits the Setup utility and restarts your computer.

SECTION 4

DRIVER UTILITY INSTALLATION

4.1 FSC-1712LD2N/LN Driver Installation

◆ Easy Driver Installation:

Step 1: To put the utilities into the CD-ROM, then it will auto run and show the figure below:

Step 2: Select and click the FSC-1712LD2N model.

Step 3: Click the Chipset Driver Installation.

Step 4: To click that enables the **Intel[®] 845 Chipset** to be recognized by listed operating systems. This installer will unpack updated **.INF** files into a specified folder. Supported operating systems:Microsoft Windows[®] 95 OSR 2.1+, Windows[®] 98 / 98SE / 98ME and Windows[®] NT2000 operating systems. This procedure will Re-start the system.

Step 5: Click the INTEL® Ultra Storage Driver to install the IDE Driver.

Step 6: Install the driver of features:

(1) LCD/VGA Function Driver Installation:

◆ If your product is <u>"FSC-1712LD2N/LN"</u>, please click the **ATI**[®] **RAGE Mobility M6 Driver** to install the LCD / VGA function.

(2) Dual LAN Function Driver Installation:

♦ If your product is <u>"FSC-1712LD2N/LN"</u>, please click the INTEL[®] 82559 (LAN2) & INTEL[®] 82562EM (LAN1) Driver to install the dual LAN function.

(3) Single LAN Function Driver Installation:

◆ If your product is <u>"FSC-1712LD2N/E"</u>, please click the INTEL[®] 82562EM (LAN1) Driver to install the single LAN function.

Note: If user cannot install CD-Driver, that user may copy file of driver to Hard Disk, then click the "Setup" file to install.

Step 7: Click the Main menu icon to return main menu.

Step 8: To click the **Additional Software** into the **NORTON GHOST ANTIVIRUS** or **PC-CILLIN 2000** and follow the setup to finish the installation. Please do make sure to update the virus definition code regularly to protect your system from virus damage.

